

In re the Application of:

TAKAYAMA, Toshio, et al.

Group Art Unit: 2811

Serial No.: 10/735,886

Examiner: Nitin PAREKH

Filed: December 16, 2003

P.T.O. Confirmation No.: 5693

For: SEMICONDUCTOR DEVICE HAVING A MULTILAYER INTERCONNECTION

STRUCTURE AND FABRICATION PROCESS THEREOF

## **AMENDMENT UNDER 37 CFR §1.111**

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

April 29, 2005

Sir:

In response to the Office Action dated January 14, 2005, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 8 of this paper.